

ChipIntelli
成都启英泰伦科技有限公司
Chipintelli Technology Co.,Ltd.

Name : CI1324X Low cost reference design

Features: CI1324X single-mic differential input, voice prompt output

Applications: general

More info: <https://document.chipintelli.com>

Release date:

Note: It is important that during both the application design and design verification stages, customers carefully read the application notes and design considerations provided in the reference design schematics, and maintain close communication with our FAE team.

ChipIntelli 成都启英泰伦科技有限公司 <small>CHIPINTELLI TECHNOLOGY CO., LTD.</small>		
Title : Front Cover		
Size B	Document Number <Doc>	Rev 1.3
Date: Tuesday, July 02, 2024	Sheet	1 of 3

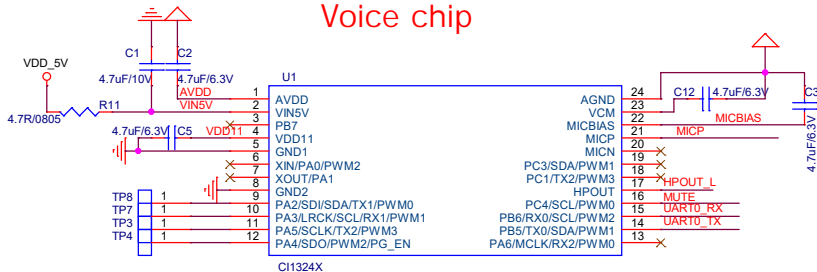
版本	修改记录	作者
V1.0	初版	启英泰伦

Title : Revision History

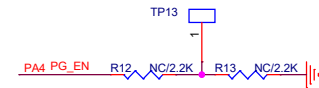
Size A	Document Number <Doc>	Rev 1.0
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Date: Tuesday, July 02, 2024 Sheet 2 of 3

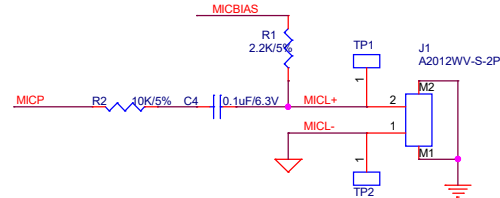
Voice chip



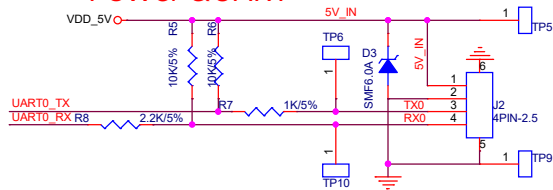
R5	R6	说明
NC	NC	上电默认升级模式，开机时间约850ms 适用于对上电时间要求不高的产品
2.2K	2.2K	上电正常工作模式，开机时间约350ms， 短接测试点后上电可以进入升级模式 适用于对上电时间有要求的产品



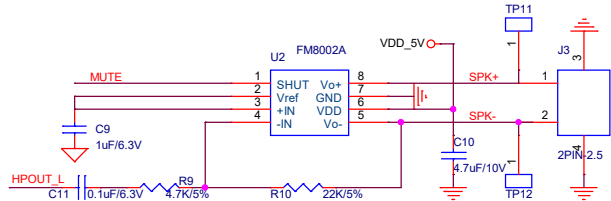
MIC



Power & UART



Class AB Amplifier



CI1324X Low cost reference design

ChipIntelli 芯朋微集成电路有限公司
Chipin Micro Technology Co., Ltd.

Title		
Size	Document Number	Rev
Custom	<Doc>	1.0
Date:	Thursday, July 25, 2024	Sheet 3 of 3